D C B A

C5 is only installed if necessary due to susceptibility to noise. Alternately, R13 and R14 can be installed for additional hysteresis sufficient to suit the application.

Input pulse duration of 100ns or more should give satisfactory performance. 50% duty cycle or more not recommended. C5 is only installed if necessary due to susceptibility to noise. Alternately, R13 and R14 can be installed for additional hysteresis sufficient to suit the application.

Do not exceed ~15V on the negative bias line as the Vgs Max limit is 20V. Voltages higher than 10V are unlikely to improve performance.

April 22, 2003, G. Przybylski

Changes:
1) The pin output on the JP1 Header should be the following:
   Pin 10 and 8  B+
   Pin 6 and 4  TT L+
   Pin 5 and 1  TT L-
   Pin 2  +5 V
   Pin 7  -5 V
   Pin 2 and 9  ground
2) Add 1 resistor parallel to the LED (0805 Footprint if possible)
3) Make connection channels to Pads 5 and 7 rather than have it connected on all 4 sides.
4) Move hole between R10 and C8 further away from pads
5) Move hole between R3 and C7 further away from pads
6) Change 5V to Mul AN

The MAX9013 is a possible alternative to the MAX961, however the footprint is not identical.